



(Use as many sheets as necessary)

Sheet	1	of	2
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Application Number	10/790,953
Filing Date	March 2, 2004
First Named Inventor	Larry D. Seiler
Art Unit	2857
Examiner Name	na
Attorney Docket Number	00100.02.0004

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3/28/06

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STATEMENT BY APPLICANT**

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Complete if Known

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Sheet

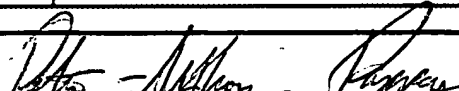
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of

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
PSP		International Search Report for International Application No. PCT/US2005/006744; European Patent Office; dated June 21, 2005.	—
PSP		Per-Fragment Operations and the Framebuffer; pages 141-148; Version 1.2.1; April 1, 1999.	—
PAP		GREENE, Ned; Hierarchical Polygon Tiling with Coverage Masks; Computer Graphics Proceedings; August 4, 1996; pages 65-74.	—
PSP		CHEN, Cheng-Hsien et al.; Two-Level Hierarchical Z-Buffer for 3D Graphics Hardware; National Chiao Tung University; 2002; pages 253-256; Taiwan, ROC.	—
PAP		BLYTHE, David; Programming with OpenGL: Advanced Rendering, section 14: Using the Stencil Buffer; from www.opengl.org; June 9, 2005.	✓

Examiner
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Date
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3/28/06

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